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TERMINAL DISCLAIMER TO OBVIATE A PROVISIONAL DOUBLE PATENTING			Docket Number (Optional)
REJI	ECTION OVER A PENDING SECOND	APPLICATION	F98ED0762
In re Application of: Hiroki NAKAMURA SFP 0 8 2003			
In re Application of: Hiroki NAKAMURA ( SEP) 0 8 2003			
Filed:	July 25, 2000	*E	
For: SEMICONDUCTOR DEVICE PROPERTY WIRING PATTERNS AND DUMMY			
PATTERNS COVERED WITH INSULATING LAYER			
The owner*, Oki Electric Industry Co., Ltd., of 1 0 0 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend			
beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 and 173 as shortened by any terminal			
disclaimer filed prior to the grant of any patent granted on pending second Application Number <u>10/128,244</u> , filed on April 24, 2002, of any patent on the pending second application. The owner hereby agrees that any patent so			
granted on the instant application shall be enforceable only for and during such period that it and any patent granted on the			
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